Voltages Across an Area of a Network

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Abstract—We define the voltage across an area of a resistive network by suitably combining voltages at buses (nodes) on the border of the area. The new voltage across the area satisfies circuit laws. The same concept works to define the voltage angle difference across an area of a DC load flow network and the complex voltage difference across an area of an AC load flow network. We first define the voltage across a cutset of lines, and then derive and explain the voltages across areas, including generalizations to several voltages across an area and multiple areas. The new voltages across areas seem promising for both power system monitoring and network reduction, and we describe their application to monitoring area stress.

Index Terms—Circuit analysis, network theory, phasor measurement units, power grids, power system modeling, smart grids.

I. INTRODUCTION

T HE difference of voltages at two buses or nodes is fundamental to power system engineering. For example, bus voltages are the difference between voltage at a bus and the voltage at a reference bus, and the voltage across a transmission line is the difference between the voltages at the buses at each end of the line. The goal of this paper is to introduce new concepts of voltage differences between sets of buses. First we define the voltage difference between sets of buses separated by a cutset of lines and then we show how to extend this definition to voltages across an area of a power system. We also define area conductance and the current through the area in a natural way. The new electrical quantities for the area satisfy circuit laws and are expected to be useful in monitoring, analyzing, and reducing power transmission networks.

We develop the voltages across cutsets or areas starting from the basic ingredients of electrical circuits: a network, a variable "across" the transmission lines such as voltage, a variable "through" the lines such as current, an "admittance-like" property of the lines such as conductance, and an "Ohm's law" relating these quantities [1]. Except for notation, the development is the same for any choice of the across, through, and admittance-like quantities. Table I shows three useful choices of these quantities for electric power engineering. Since the resistive circuit is the most basic electrical circuit, the paper develops the cutset and area voltages using the resistive circuit notation, but

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TABLE I CIRCUIT QUANTITIES

circuit type	"across" variable	"through" variable	"admittance"	"Ohm's law"
Resistive DC load flow AC load flow	voltage V angle θ voltage V all AC load	current I power P current I flow circuit	conductance g susceptance b admittance y quantities are co	$I = gV$ $P = b\theta$ $I = yV$ mplex

it is trivial to substitute the appropriate notation to obtain the corresponding results for the DC and AC load flow networks.

The applications of area voltages in power systems include model reduction and synchrophasor monitoring of area stress and line trips [2]-[4]. The concept of voltage phasor angle across a cutset of lines seems to be first introduced in [2]. The cutset voltage angle is extended to measure voltage angles across "cutset areas" in [3]. Cutset areas are special areas that separate the other areas of the power system from each other. The voltage angles across cutset areas can be monitored by synchrophasor measurements at the border buses and used to monitor cutset area stress and whether line trips occur in the cutset area [3]. The conference paper [4] defines voltage angles across more general areas and describes their application to monitor area stress and to detect line trips. In this paper, motivated by these applications, we completely rework and generalize the theory of voltages across power system areas, and describe their application to monitor area stress.

II. VOLTAGE ACROSS A CUTSET

A. Example of Cutset Voltage

Consider the example resistive network on the left-hand side of Fig. 1. There are current injections at the buses (nodes) that cause current flows and voltage differences in the network. The network buses $M = \{1, 2, 3, 4, 5\}$ are partitioned into two subsets of buses $M_a = \{1, 2, 3\}$ and $M_b = \{4, 5\}$. The buses in M_a are separated from the buses in M_b by the cutset of lines 1,2,3,4,5,6 that is indicated by the dashed line. (Recall from circuit theory that a *cutset* of lines is a set of lines that cuts the network into separate networks when that set of lines is removed from the network.¹) The objective is to define the voltage \hat{V}_{ab} across the cutset from the buses in M_a to the buses in M_b that satisfies Ohm's law.

Write V_i for the voltage at bus number i and \hat{V}_j for the voltage difference across line number j. The conductance of line number j is g_j . The current flowing from M_a to M_b along line j of the cutset is $g_j\hat{V}_j$. For this example, it is convenient to assume that the voltage difference \hat{V}_j on line j is positive for positive current flowing on line j from M_a to M_b . The current

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¹Some authors define a cutset to be a *minimal* set of buses or lines that separate the network, but we do not require this here.



$$c_j = \begin{cases} 1 & \text{line } j \text{ in cutset has sending end in } M_a \\ -1 & \text{line } j \text{ in cutset has receiving end in } M_a \\ 0 & \text{line } j \text{ not in cutset.} \end{cases}$$

The line conductances $g_1, g_2, \ldots, g_{nline}$ are assumed to be positive. The diagonal matrix of line conductances is

$$\Lambda = \operatorname{diag}\{g_1, g_2, \dots, g_{\operatorname{nline}}\}.$$

Write the vector of voltages across the lines as \hat{V} . Then the current flow through the cutset from M_a to M_b is

$$I_{ab} = \sum_{j} c_j g_j \hat{V}_j = c \Lambda \hat{V}.$$
(5)

Define the conductance of the cutset

a

$$g_{ab} = \sum_{j} g_j = c\Lambda c^T.$$
 (6)

The cutset conductance g_{ab} is positive since the line conductances are positive and the connectedness of the network implies that c is not zero. One consequence of (6) is that if a line is not in the cutset, its conductance is not included in the cutset conductance.

Define the voltage across the cutset from M_a to M_b as

$$\hat{V}_{ab} = \frac{c\Lambda\hat{V}}{g_{ab}}.$$
(7)

Then (5), (6), and (7) imply Ohm's law for the cutset

$$I_{ab} = g_{ab}\hat{V}_{ab}.$$
(8)

Now we express the formulas for cutset voltage, conductance, and current in terms of the bus voltages. Let V_m be the column vector of bus voltages. A is the incidence matrix

$$A_{ij} = \begin{cases} 1 & \text{bus } i \text{ is sending end of line } j \\ -1 & \text{bus } i \text{ is receiving end of line } j \\ 0 & \text{otherwise} \end{cases}$$

that relates bus voltages V_m to the line voltage differences V:

$$\hat{V} = A^T V_m. \tag{9}$$

The row vector σ_a defines the buses in M_a by

$$(\sigma_a)_i = \begin{cases} 1 & \text{bus } i \text{ in } M_a \\ 0 & \text{otherwise.} \end{cases}$$
(10)

Then the lines in the cutset are given by

$$c = \sigma_a A \tag{11}$$

and the network conductance matrix is3

$$G_{mm}^{\rm red} = A\Lambda A^T.$$
 (12)



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 I_{ab} flowing through the cutset from M_a to M_b is the sum of the currents flowing in each line of the cutset:

$$I_{ab} = g_1 \hat{V}_1 + g_2 \hat{V}_2 + g_3 \hat{V}_3 + g_4 \hat{V}_4 + g_5 \hat{V}_5 + g_6 \hat{V}_6.$$
(1)

The cutset conductance is defined to be

is contracted to bus a and M_b is contracted to bus b.

$$g_{ab} = g_1 + g_2 + g_3 + g_4 + g_5 + g_6.$$
 (2)

This definition of cutset conductance is expected because the cutset lines are in parallel. We define the voltage across the cutset from M_a to M_b as

$$\hat{V}_{ab} = \frac{g_1}{g_{ab}}\hat{V}_1 + \frac{g_2}{g_{ab}}\hat{V}_2 + \frac{g_3}{g_{ab}}\hat{V}_3 + \frac{g_4}{g_{ab}}\hat{V}_4 + \frac{g_5}{g_{ab}}\hat{V}_5 + \frac{g_6}{g_{ab}}\hat{V}_6 \quad (3)$$

which is a linear combination of the cutset line voltage differences, weighted according to the line conductances. Then (1), (2), and (3) imply Ohm's law for the cutset $I_{ab} = g_{ab}\hat{V}_{ab}$.

Equation (3) can be rewritten in terms of the bus voltages V_1, V_2, V_3, V_4, V_5 as

$$\hat{V}_{ab} = \frac{g_1 + g_2}{g_{ab}} V_1 + \frac{g_3 + g_4}{g_{ab}} V_2 + \frac{g_5 + g_6}{g_{ab}} V_3 - \frac{g_1 + g_3 + g_5}{g_{ab}} V_4 - \frac{g_2 + g_4 + g_6}{g_{ab}} V_5.$$
(4)

This example shows how a cutset voltage can be defined to be consistent with Ohm's law. Defining the voltage, current and conductance of the cutset can be regarded as contracting the network² to a single line equivalent joining two buses as illustrated in Fig. 1.

B. General Case of Cutset Voltage

More generally, consider a connected resistive network in which the buses M of the network are partitioned into two subsets M_a and M_b . Let c be the cutset of lines in the subnetwork



³The superscript red stands for "reduced" and is included in the notation for compatibility with Section III. The network considered in this section becomes a Kron-reduced subnetwork of border buses M in Section III.

²This operation is called vertex contraction in graph theory.

The conductance matrix G_{mm}^{red} is symmetric and its rows add to zero. Moreover, since the network is assumed to be connected, G_{mm}^{red} is irreducible. Now, using (11), the cutset conductance g_{ab} defined in (6) is re-expressed as

$$g_{ab} = \sigma_a G_{mm}^{\text{red}} \sigma_a^T \tag{13}$$

and the cutset voltage \hat{V}_{ab} defined in (7) is re-expressed as

$$\hat{V}_{ab} = \frac{\sigma_a G_{mm}^{\text{red}} V_m}{g_{ab}}.$$
(14)

Moreover, if we write I_m^{red} for the column vector of bus current injections, then

$$I_{ab} = \sigma_a I_m^{\text{red}} \tag{15}$$

is both the total current injected in buses M_a and the current that flows from M_a to M_b through the cutset c.

The Ohm's law equations for the network are

$$I_m^{\rm red} = G_{mm}^{\rm red} V_m. \tag{16}$$

The Ohm's law (8) for the cutset can now be derived in a direct way by multiplying (16) by σ_a :

$$I_{ab} = \sigma_a I_m^{\text{red}} = \sigma_a G_{mm}^{\text{red}} V_m = g_{ab} \frac{\sigma_a G_{mm}^{\text{red}} V_m}{g_{ab}} = g_{ab} \hat{V}_{ab}.$$
(17)

It is possible that some buses in M_a do not have any incident lines in the cutset c. The entries of the row vector $\sigma_a G_{mm}^{\text{red}}$ corresponding to these buses are zero⁴ and it follows that the voltages of these buses do not appear in the formula (7) for the cutset voltage. This also holds for buses in M_b that do not have any incident lines in the cutset c.

The cutset voltage can also be derived as a novel choice of coordinates in classical network theory. This is explained in Appendix A.

III. VOLTAGE ACROSS AN AREA

We define the voltage \hat{V}_{ab} between a set of buses M_a and a set of buses M_b and across an area R of a resistive network. We also define the current flow I_{ab} through area R from M_a to M_b and the conductance g_{ab} of area R from M_a to M_b . The sets of buses M_a and M_b are assumed to be nonempty and have no buses in common. We write $M = M_a \bigcup M_b$ for the buses in M_a or M_b . The buses M in this section correspond to all of the buses of the network considered in Section II.

The area R is a subnetwork that we assume to be connected. The buses in R are partitioned into border buses M and interior buses N. The interior buses N must have no incident lines joining them to buses outside R.⁵ Each border bus in M can have incident lines joining it to buses outside R, but this is not required. Fig. 2 shows an example of an area R in the 39-bus New England test system. There are usually multiple ways to divide the border buses M into the buses M_a and M_b . For example, in Fig. 2, we can choose $M_a = \{31, 32\}$ and $M_b = \{39, 4, 14\}$, or we can choose $M_a = \{31, 32, 39\}$ and $M_b = \{4, 14\}$. Each choice of M_a and M_b gives different quantities V_{ab} , I_{ab} , and g_{ab} . That is, the electrical properties from one side of an area to the other side of the area depend on how the "sides" are chosen.

Recall from circuit theory that a *nodal cutset* of buses is a set of buses that cuts the network into separate networks when that set of buses is removed from the network. It is usual and useful to choose M to be a nodal cutset. If M is a nodal cutset, then the network that remains when the buses in M are removed from the network has at least two components. One or several of these components can be chosen as the interior buses N. However, to avoid buses in N being directly connected to buses outside R, the buses in each of these components are either all included in N or all excluded from N. (If M is not a nodal cutset, then the network that remains when the buses in M are removed from the network is connected and must be all of N, and area R must be the entire network. The theory works for this case, but it is less useful.)

The next step is to write the circuit equations for area R, with the effect of the rest of the network replaced by the currents flowing into area R from the rest of the network. We use the following notation for column vectors of voltages and currents:

V_m	voltages at border buses M ;
I_m	injected currents at border buses M ;
V_n	voltages at interior buses N ;
I_n	injected currents at interior buses N ;
$V_{\overline{r}}$	voltages at buses outside R;
$I_{\overline{r}}$	injected currents at buses outside R.

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The currents into border buses M along lines not in R are

$$I_m^{\text{into}} = \sum_{j \in \overline{r}} (-G_{mj})(V_j - V_m)$$

= $-G_{m\overline{r}}V_{\overline{r}} + \text{diag}\{G_{m\overline{r}}\mathbf{1}\}V_m.$ (18)

Here 1 is a row or a column vector of all ones.⁶ Order the buses so that all the border buses M are first, all the interior buses Nare second, and all the buses outside R are third. Then Ohm's law I = GV for the entire network is

$$\begin{pmatrix} I_m \\ I_n \\ I_{\overline{r}} \end{pmatrix} = \begin{pmatrix} G_{mm} & G_{mn} & G_{m\overline{r}} \\ G_{nm} & G_{nn} & 0 \\ G_{\overline{r}m} & 0 & G_{\overline{rr}} \end{pmatrix} \begin{pmatrix} V_m \\ V_n \\ V_{\overline{r}} \end{pmatrix}.$$
 (19)

The first two block rows of (19) may be rewritten as

$$\begin{pmatrix} I_m \\ I_n \end{pmatrix} = \begin{pmatrix} G_{mm}^{\rm R} & G_{mn} \\ G_{nm} & G_{nn} \end{pmatrix} \begin{pmatrix} V_m \\ V_n \end{pmatrix} - \begin{pmatrix} I_{m}^{\rm into} \\ 0 \end{pmatrix}$$
(20)

where
$$G_{mm}^{\mathrm{R}} = G_{mm} + \operatorname{diag}\{G_{m\overline{r}}\mathbf{1}\}.$$
 (21)

The diagonal entries of $G_{mm}^{\rm R}$ are different than the submatrix G_{mm} of the conductance matrix for the entire network, because

⁶The dimension of 1 varies with context and is chosen so that it can be multiplied by the matrix next to it in the formula. For example, given an 3×4 matrix $X, X\mathbf{1} = \sum_{j=1}^{4} X_{ij}$ is the column vector formed by adding the columns of X, and $\mathbf{1}X = \sum_{i=1}^{3} X_{ij}$ is the row vector formed by adding the rows of X.

⁴A column w of G_{mm}^{red} corresponding to buses in M_a with no incident lines in the cutset c has zeros in the rows corresponding to M_b . Therefore, $\sigma_a w$ is the sum of all the nonzero entries of w and $\sigma_a w = 0$ since $1G_{mm}^{red} = 0$.

⁵This section assumes that there is at least one interior bus since Section II treats the case of no interior buses.



Fig. 2. Area R in the 39-bus New England test system. Interior buses $N = \{5, 6, 7, 8, 9, 10, 11, 12, 13\}$ are shown with thick circles. Border buses $M = \{31, 32, 39, 4, 14\}$ are shown with thick squares.



Fig. 3. Kron reduction of area R followed by contraction to two buses.

TABLE II		
EXAMPLE RESULTS FOR AREA R CONTRACTED TO	Two	BUSES

DC load flow				
$M_a = \{31, 32\}$	$\hat{\theta}_{ab} = 10.07$	$P_{ab} = 9.82$	$5 b_{ab} = 55.88$	
$M_b = \{39, 4, 14\}$				
$\hat{\theta}_{ab} = 0.4587\theta_{31} + 0$	$0.5413\theta_{32} - 0.0$	$863\theta_{39} - 0.4$	$1167\theta_4 - 0.4970\theta_{14}$	
	AC load	flow		
$M_a = \{31, 32\}$	$V_a = 1.028 \angle 1$	1.66 $V_b = 0$	$0.9601 \angle 0.7634$	
$M_b = \{39, 4, 14\}$	$y_{ab} = 1.614 -$	- 56.57 <i>j</i>		
$y_{ag} = 0.0434 + 1.$.035j	$y_{bg} = -0$	0.040 + 5.394j	
$V_a = (0.4858 \angle -0.4)$	$(0)V_{31} + (0.514)$	$3\angle 0.38)V_{32}$		
$+(0.2058\angle -0.01)V$	$V_{39} + (0.0687 \angle$	$(2.11)V_4 + (0)$	$.2744\angle -179.5)V_{14}$	
$V_b = (0.0274 \angle -2.8)$	$(0.027)V_{31} + (0.027)$	$4 \angle 177.1 V_{32}$		
$+(0.2899\angle -0.26)V$	$\sqrt{39} + (0.4862 \angle)$	$-0.11)V_4 + ($	$(0.2239\angle 0.58)V_{14}$	

angles in degrees, all other quantities in per unit; g is ground node

they do not include the conductances of the lines outside R that are joined to buses in M.

The currents entering the border buses of area R are the sum of the currents injected directly at the buses and the currents along the lines not in R that are joined to the buses:

$$I_m^{\rm R} = I_m + I_m^{\rm into}.$$
 (22)

Now (20) may be rewritten as

$$\begin{pmatrix} I_m^{\rm R} \\ I_n \end{pmatrix} = \begin{pmatrix} G_{mm}^{\rm R} & G_{mn} \\ G_{nm} & G_{nn} \end{pmatrix} \begin{pmatrix} V_m \\ V_n \end{pmatrix} = G^{\rm R} \begin{pmatrix} V_m \\ V_n \end{pmatrix}$$
(23)

which is Ohm's law written for area R. G^{R} is the conductance matrix for the area R considered as a stand-alone network.

The next step reduces area R to an equivalent subnetwork. We retain the border buses M and apply a standard Kron reduction⁷ to eliminate the interior buses N. Eliminating V_n from (23) gives

$$I_{m}^{R} - G_{mn}G_{nn}^{-1}I_{n} = \left(G_{mm}^{R} - G_{mn}G_{nn}^{-1}G_{nm}\right)V_{m}$$

and, letting

$$I_m^{\text{Rred}} = I_m^{\text{R}} - G_{mn} G_{nn}^{-1} I_n \tag{24}$$

$$G_{mm}^{\text{Rred}} = G_{mm}^{\text{R}} - G_{mn}G_{nn}^{-1}G_{nm}$$
(25)

we obtain a reduced subnetwork Rred electrically equivalent to R with the Ohm's law

$$I_m^{\text{Rred}} = G_{mm}^{\text{Rred}} V_m.$$
⁽²⁶⁾

The reduced subnetwork Rred is the border buses M joined by additional equivalent transmission lines and with additional current injections included in I_m^{Rred} to account for the lines of the network outside R that inject currents into R.

The reduced subnetwork Rred consists of the border buses M_a separated from the border buses M_b by a cutset of lines exactly as considered in Section II. We now apply to Rred the formulas (14), (13), and (15) to define voltage \hat{V}_{ab} across the area from M_a to M_b , the area conductance g_{ab} , and the current I_{ab} through the area from M_a to M_b . That is, the reduced subnetwork Rred becomes a single line equivalent in which M_a is contracted to a single bus a and M_b is contracted to a single bus b. The voltage across area R is the voltage \hat{V}_{ab} between the buses of the single line equivalent, the conductance of area R is the current I_{ab} . The process of Kron reduction followed by contraction to the single line is illustrated in Fig. 3. Since both the Kron reduction and the contraction are consistent with circuit laws, the resulting area quantities satisfy Ohm's law (8).

An example of computational results for area R of the New England test system for DC and AC load flows is shown in Table II. (Parameters of the New England test system are specified in Appendix D.) The DC and AC load flows apply the resistive network results to different electrical quantities as shown in Table I. The voltage across area R is computed as the weighted combination of the voltages at the border buses shown in Table II. The weights correspond to (14). The voltage angle across area R is 10.1 degrees for the DC load flow and 10.9 degrees for the AC load flow. The AC load flow network includes shunts to a ground node denoted by g. Therefore, the single line equivalent also has shunts to the ground node. Accommodating the shunts and the ground node in the analysis is discussed in Appendix B.

IV. GENERALIZING CUTSET VOLTAGE TO VOLTAGES FOR SEVERAL SETS OF BUSES

In Section II, we considered a connected resistive network in which the buses were partitioned into two subsets M_a and M_b . The cutset voltage defined a voltage between two subsets

⁷According to [5, Theorem (5,7)] and [6, Lemma 2.1], for a real conductance matrix $G^{\rm R}$, area R connected implies that $G^{\rm R}$ has rank one less than the number of buses in R, G_{nn} is not singular, and the Kron reduction is valid. We thank F. Dörfler for providing these references. Kron reduction is also known as a Ward equivalent or a Schur reduction.



Fig. 4. Contraction of a network to three buses.

of buses M_a and M_b . As illustrated in Fig. 1, the network was contracted to two buses joined by a single line equivalent.

Now we generalize by partitioning the network into three nonempty subsets M_a, M_b, M_c and obtaining the voltages of these three subsets of buses and the conductances between them. This contracts the network to three buses joined by three equivalent lines. The *a* bus of the reduced network corresponds to the buses M_a of the original network, the *b* bus corresponds to M_b , and the *c* bus corresponds to M_c as illustrated in Fig. 4. We want to calculate the voltages V_a, V_b, V_c of the three buses, the conductances g_{ab}, g_{bc}, g_{ca} of the three equivalent lines, and the equivalent currents I_a, I_b, I_c injected at the three buses.

We write

$$V_A = \begin{pmatrix} V_a \\ V_b \\ V_c \end{pmatrix} \quad \text{and} \quad I_A = \begin{pmatrix} I_a \\ I_b \\ I_c \end{pmatrix}. \tag{27}$$

The conductance matrix of the equivalent 3 bus network is

$$G_A = \begin{pmatrix} g_{ab} + g_{ca} & -g_{ab} & -g_{ca} \\ -g_{ab} & g_{ab} + g_{bc} & -g_{bc} \\ -g_{ca} & -g_{bc} & g_{bc} + g_{ca} \end{pmatrix}.$$
 (28)

The matrix σ_A defines the buses in each of M_a, M_b, M_c by

$$(\sigma_A)_{ij} = \begin{cases} 1 & \text{bus } j \text{ in } M_a \text{ and } i = 1\\ 1 & \text{bus } j \text{ in } M_b \text{ and } i = 2\\ 1 & \text{bus } j \text{ in } M_c \text{ and } i = 3\\ 0 & \text{otherwise.} \end{cases}$$
(29)

For example, the first row of σ_A defines the buses in M_a and corresponds to the row vector σ_a in (10). As in Section II-B, the bus voltages are V_m , the bus current injections are I_m^{red} , the network conductance matrix is G_{mm}^{red} , and the Ohm's law equations for the network are

$$I_m^{\rm red} = G_{mm}^{\rm red} V_m. \tag{30}$$

The injected currents I_a at bus a of the three-bus network must be the sum of the currents injected at the buses in M_a . Similarly, I_b is the sum of the currents injected at the buses in M_b and I_c is the sum of the currents injected at the buses in M_c . This can be written

$$I_A = \sigma_A I_m^{\text{red}}.$$
 (31)

We obtain the conductance matrix of the three bus network as

$$G_A = \sigma_A G_{mm}^{\text{red}} \sigma_A^T. \tag{32}$$

It can be seen that (32) generalizes (13). It follows from (32) and G_{mm}^{red} irreducible that G_A is irreducible. Moreover, G_A has rank two.⁷

The voltage V_A is defined to be a solution to

$$G_A V_A = \sigma_A G_{mm}^{\text{red}} V_m. \tag{33}$$

Allowing for the different position in the equation of the conductance matrix G_A , it can be seen that (33) generalizes (14). It follows from (33) that multiplying (30) by σ_A yields

$$I_A = \sigma_A I_m^{\text{red}} = \sigma_A G_{mm}^{\text{red}} V_m = G_A V_A \tag{34}$$

which is Ohm's law for the three-bus network.

Now we discuss solving (33) for V_A . Although G_A is singular, (33) can be solved in a standard way. First note that since $\mathbf{1}\sigma_A = \mathbf{1}$ and the rows of G_{mm}^{red} sum to zero

$$\mathbf{1}\sigma_A G_{mm}^{\mathrm{red}} = \mathbf{1}G_{mm}^{\mathrm{red}} = 0. \tag{35}$$

Then the components of the right-hand side of (33) sum to zero and are in the range of G_A . Moreover, since G_A has rank two, (33) is solvable for V_A to within a constant added to each component of V_A . The constant is determined by the choice of zero reference voltage. For example, we can require the solution V_A to have its third component $V_c = 0$. Then we can calculate the first two components V_a and V_b by solving the equations formed by deleting the last row of (33).

The calculation of V_A , I_A , and G_A is given here for the case in which the buses are partitioned into three subsets M_a , M_b , and M_c , but it is clear that the corresponding calculation for two subsets or for more than three subsets is the same except for the dimensions of the vectors and matrices. We now calculate V_A in the case of two subsets M_a and M_b in order to make it clear that it gives the same voltage difference $\hat{V}_{ab} = V_a - V_b$ that is the cutset voltage calculated in Section II-B.

Suppose that the network buses are partitioned into two subsets M_a, M_b . The conductance matrix of the equivalent single line and two bus network is

$$G_A = \begin{pmatrix} g_{ab} & -g_{ab} \\ -g_{ab} & g_{ab} \end{pmatrix}.$$
 (36)

The rows of the matrix σ_A are the row vectors σ_a and σ_b :

$$\sigma_A = \left(\frac{\sigma_a}{\sigma_b}\right). \tag{37}$$

First we note that

$$I_A = \begin{pmatrix} I_a \\ I_b \end{pmatrix} = \sigma_A I_m^{\text{red}} = \begin{pmatrix} \sigma_a \\ \sigma_b \end{pmatrix} I_m^{\text{red}} = \begin{pmatrix} I_{ab} \\ I_{ba} \end{pmatrix}$$
(38)

agrees with the definition of I_{ab} in (8). Now we consider the other quantities. The conductance matrix of the single line equivalent is

$$G_A = \sigma_A G_{mm}^{\text{red}} \sigma_A^T = \begin{pmatrix} \sigma_a G_{mm}^{\text{red}} \sigma_a^T & \sigma_a G_{mm}^{\text{red}} \sigma_b^T \\ \sigma_b G_{mm}^{\text{red}} \sigma_a^T & \sigma_b G_{mm}^{\text{red}} \sigma_b^T \end{pmatrix}.$$

Since $\sigma_b = 1 - \sigma_a$

$$G_A = \begin{pmatrix} \sigma_a G_{mm}^{\text{red}} \sigma_a^T & -\sigma_a G_{mm}^{\text{red}} \sigma_a^T \\ -\sigma_a G_{mm}^{\text{red}} \sigma_a^T & \sigma_a G_{mm}^{\text{red}} \sigma_a^T \end{pmatrix} = \begin{pmatrix} g_{ab} & -g_{ab} \\ -g_{ab} & g_{ab} \end{pmatrix}.$$

Equation (33) becomes

$$\begin{pmatrix} g_{ab} & -g_{ab} \\ -g_{ab} & g_{ab} \end{pmatrix} \begin{pmatrix} V_a \\ V_b \end{pmatrix} = \begin{pmatrix} \sigma_a G_{mm}^{\text{red}} \\ \sigma_b G_{mm}^{\text{red}} \end{pmatrix} V_m$$
$$= \begin{pmatrix} \sigma_a G_{mm}^{\text{red}} \\ -\sigma_a G_{mm}^{\text{red}} \end{pmatrix} V_m$$



Fig. 5. Kron reduction of area R followed by contraction to three buses.

 TABLE III

 Example Results for Area R Contracted to Three Buses

DC load flow				
$M_a = \{31, 32\}$	$\theta_a = 9.141$	$P_a = 9.825$		
$M_b = \{39, 4\}$	$\theta_b = -1.855$	$P_b = -8.344$		
$M_c = \{14\}$	$\theta_c = 0$	$P_c = -1.482$		
$b_{ab} =$	28.11 $b_{bc} = 91$	$.06 b_{ca} = 27.77$		
$\theta_a = 0.4308\theta_{31}$	$+0.5692\theta_{32}-0$	$0.0637\theta_{39} + 0.0637\theta_4 - \theta_{14}$		
$\theta_b = -0.0554\theta_{31}$	$+0.0554 heta_{32}+0$	$0.0450\theta_{39} + 0.9551\theta_4 - \theta_{14}$		

angles in degrees, all other quantities in per unit

and multiplying both sides by the row vector (1/2, -1/2) yields

$$g_{ab}(V_a - V_b) = \sigma_a G_{mm}^{\text{red}} V_m. \tag{39}$$

Hence, comparing (39) with (14), $\hat{V}_{ab} = V_a - V_b$.

V. GENERALIZING VOLTAGE ACROSS AN AREA TO MULTIPLE VOLTAGES ACROSS AN AREA

Given the generalization of the cutset voltage to voltages for several sets of buses in Section IV, it is straightforward to generalize to multiple voltages across an area. We consider the case of three voltages. Following the pattern of Section III, choose a connected area R with border buses M. Partition the border buses into subsets of buses M_a, M_b, M_c . As in Section III, form the conductance matrix $G^{\rm R}$ of the area R and apply Kron reduction to eliminate the interior buses. The result is the equivalent network Rred with border buses $M = M_a \bigcup M_b \bigcup M_c$ joined by equivalent lines with bus voltages V_m , equivalent bus current injections $I_m^{\rm red}$, and equivalent conductance matrix $G_{mm}^{\rm Rred}$. $G_{mm}^{\rm Rred}$ is guaranteed to inherit the symmetric and irreducible properties of $G^{\rm R}$ [6, Lemma 2.1]. Now apply the calculations of Section IV to this Kron-reduced subnetwork to obtain voltages V_a, V_b, V_c , line conductances g_{ab}, g_{bc}, g_{ca} , and current injections I_a, I_b, I_c for area R.

For example, in Fig. 5, the Kron reduction of area R is contracted to three buses. A DC load flow example computation of the voltage angles, power injections, and susceptances of the three-bus network is shown in Table III.

VI. SIMULTANEOUS REDUCTION OF MULTIPLE AREAS

The previous sections have analyzed a single area. In model reduction, or for monitoring multiple areas, it can be convenient to obtain a reduced model of the entire network by reducing multiple areas simultaneously. This section discusses the simultaneous reduction for the three areas R_{γ} , R_{α} , R_{β} shown in



Fig. 6. Areas $R_{\gamma} = N_{\gamma} \cup M_a \cup M_b$, $R_{\alpha} = N_{\alpha} \cup M_b \cup M_c$, and $R_{\beta} = N_{\beta} \cup M_c \cup M_a$.

Fig. 6. Write $M = M_a \cup M_b \cup M_c$ for the set of all the border buses of all three areas and $N = N_\gamma \cup N_\alpha \cup N_\beta$ for all the interior buses. Each border bus is either on the border of two contiguous areas or on the border of only one area.

Order the buses so that all the border buses M come first. Then Ohm's law for the entire network is

$$\begin{pmatrix} I_m \\ I_n \end{pmatrix} = \begin{pmatrix} G_{mm} & G_{mn} \\ G_{nm} & G_{nn} \end{pmatrix} \begin{pmatrix} V_m \\ V_n \end{pmatrix}.$$
 (40)

The next step is Kron reduction to an equivalent network that retains only the border buses M. The equivalent current injections and equivalent conductance matrix of the Kron reduced network are

$$I_m^{\text{red}} = I_m - G_{mn} G_{nn}^{-1} I_n \tag{41}$$

$$G_{mm}^{\rm red} = G_{mm} - G_{mn} G_{nm}^{-1} G_{nm}.$$
 (42)

The rows of the matrix σ_A define the buses in M_a, M_b, M_c according to

$$(\sigma_A)_{ij} = \begin{cases} 1 & i = 1 \text{ and bus } j \text{ in } M_a \\ 1 & i = 2 \text{ and bus } j \text{ in } M_b \\ 1 & i = 3 \text{ and bus } j \text{ in } M_c \\ 0 & \text{otherwise.} \end{cases}$$
(43)

For the voltages and injected currents injected at M_a, M_b, M_c , we write

$$V_A = \begin{pmatrix} V_a \\ V_b \\ V_c \end{pmatrix} \quad \text{and} \quad I_A = \begin{pmatrix} I_a \\ I_b \\ I_c \end{pmatrix}. \quad (44)$$

Then, with this slightly revised notation, the formulas for the voltages V_A at M_a, M_b, M_c , the conductance matrix G_A between M_a, M_b, M_c , and the injected currents I_A at M_a, M_b, M_c , are (33), (32), (31). Moreover, the Ohm's law (34) applies. Appendix C shows that this simultaneous reduction of the three areas gives the same results as considering each area separately.

For example, as applied to DC load flow and four areas, consider Fig. 7. In Fig. 7, the entire New England 39-bus system is contracted to an equivalent network with five buses. An example computation of the voltage angles, power injections, and susceptances of the five-bus network is shown in Table IV.

VII. MONITORING AREA STRESS

This section shows an application of area angles to monitor area stress from synchrophasor measurements around the border of the area.



Fig. 7. New England 39-bus test system contracted to five buses. Border buses separating areas are shown with squares.

 TABLE IV

 Example Results for Entire Network Contracted to Five Buses

	DC loa	d flow	
$M_a = \{31, 3$	$\theta_a = 0$).115	$P_a = 9.825$
$M_b = \{39, 4\}$	$\{ \theta_b = -$	-1.906	$P_b = -10.17$
$M_c = \{1, 3\}$	$\theta_c = -$	-0.7617	$P_c = 0.7745$
$M_d = \{17\}$	$\theta_d = 0$	0.1823	$P_d = 1.042$
$M_e = \{14\}$	$\theta_e = 0$)	$P_e = -1.468$
$b_{ab} = 28.11$	$b_{ac} = 0$	$b_{ad} = 0$	$b_{ae} = 27.77$
	$b_{bc} = 86.95$	$b_{bd} = 0$	$b_{be} = 91.06$
		$b_{cd} = 58.4$	$42 b_{ce} = 0$
			$b_{de} = 25.00$

angles in degrees, all other quantities in per unit

A. Background

Synchronized phasor measurements are being widely deployed for power system monitoring and control [7]. Here we assume the availability of suitable synchrophasor measurements at a control center. Tate and Overbye [8] give a good discussion of the necessary signal processing.

Previous work on monitoring power system stress with phasor measurements has focused on the angle difference between two buses. Simulations of the grid conditions before the August 2003 USA/Canada blackout show increasing angle differences between Cleveland and West Michigan, suggesting that large angle differences could be a blackout precursor [9]. Simulations of the New England grid show that angle differences are the best phasor measurements in discriminating alert and emergency states [10].

A large angle difference between two buses does indicate, in a general sense, a stressed power system. However, this angle difference is affected by changes throughout the entire grid, and it is difficult to interpret changes in the angle difference or set thresholds.

It is natural to measure the stress on an individual transmission line by the angle difference across the line. Since the preceding sections show how an area can be reduced to an equivalent transmission line, and the angle across the area is the angle across the equivalent transmission line, we can use the angle across the area as a measure of the area stress.

For further motivation, consider the DC load flow of two buses a and b joined by two equal lines. We measure the stress between a and b by the phasor angle difference $\theta_a - \theta_b$. In this simple case, the reduction to a single line is clear: $\hat{\theta}_{ab} =$ $\theta_a - \theta_b, b_{ab}$ is the sum of the line susceptances, and P_{ab} is the sum of the line power flows. $\hat{\theta}_{ab}$ measures the stress better than P_{ab} , because if one of the lines outages, then the power flow P_{ab} (now flowing only on the line remaining in service) does not change but $\hat{\theta}_{ab}$ doubles, correctly indicating the increased stress.

B. Stress Across an Area

We use a DC load flow model of an area R with border buses M_a and M_b . Then the area angle $\hat{\theta}_{ab}$ gives the stress across area R from M_a to M_b . The Ohm's law $P_{ab} = b_{ab}\hat{\theta}_{ab}$ corresponding to (8) implies that $\hat{\theta}_{ab}$ is proportional to the real power P_{ab} passing through the area and inversely proportional to the area susceptance b_{ab} .

By using Table I to substitute the corresponding DC load flow circuit quantities for the resistive circuit quantities in (14), the area angle $\hat{\theta}_{ab}$ is a weighted linear combination of the border bus angles θ_m :

$$\hat{\theta}_{ab} = w_m \theta_m$$
, where $w_m = \frac{\sigma_a B_{mm}^{\text{red}}}{b_{ab}}$. (45)

The weights in the row vector w_m are determined from the DC load flow model of R via (45), (12), and (13). Therefore, if we place phasor angle measurements at all the border buses of R and know the status of lines in R, we can easily determine the angle $\hat{\theta}_{ab}$.⁸ The availability in practice of a DC load flow model of an area is discussed in [8].

We illustrate stress across the area R shown in Fig. 8, which shows part of a 225-bus model of the Western North American power grid.⁹ The three border buses with assumed phasor measurements are $M_a = \{\text{ELDORADO}, \text{PALOVRDE}\}$ and $M_b = \{\text{VINCENT}\}$. From (45), the angle $\hat{\theta}_{ab}$ east to west across area R combines the border buses angles as

$$\theta_{ab} = 0.7987 \theta_{\text{ELDORADO}} + 0.2013 \theta_{\text{PALOVRDE}} - \theta_{\text{VINCENT}}.$$

The base case area stress is $\hat{\theta}_{ab} = 10.85$ degree and the equivalent power flow westward through the area is $P_{ab} = 1734$ MW. The area susceptance is $b_{ab} = 91.62$ per unit on a 100 MW base.

Examples of transfers and line outages are shown in Fig. 8. Table V shows the effect of these transfers and line outages on the angle $\hat{\theta}_{ab}$ across area R. The transfer inside area R increases $\hat{\theta}_{ab}$ by increasing the effective westward power flow. The transfer outside area R also increases the westward power flow through the area and $\hat{\theta}_{ab}$. The line outages also increase $\hat{\theta}_{ab}$. In particular, the line outage inside R decreases the area admittance whereas the line outage outside R has the effect of increasing the westward power transfer through the area.

C. Internal and External Stress Angles

Section VII-B shows that the area angle $\hat{\theta}_{ab}$ measures the total east-west stress across area R, and it responds to changes both inside and outside the area. Now we decompose the area angle into an internal angle due to power injections inside the

⁸Alternatively, the AC load flow version of (45) may be obtained by substituting the complex circuit quantities according to Table I. This yields an expression for the complex voltage across the area in terms of the complex voltages measured at the border buses.

⁹We thank C.-C. Liu, N. Yu, and J. Li for graciously providing this power system model.



Fig. 8. Area R shown as the black buses; all other buses are gray. The border buses with phasor measurements are the labelled black triangles. A 100 MW power transfer and a line to outage are shown both inside R (in the black buses) and outside R (in the gray buses).

TABLE V Area Angles

	$\hat{ heta}_{ab}$	$\hat{\theta}_{ab}^{\mathrm{into}}$	$\hat{\theta}^{\mathrm{inR}}_{ab}$
base case	10.85	9.776	1.070
transfer 100 MW inside R	11.10	9.765	1.330
transfer 100 MW outside R	11.45	10.38	1.070
line outage inside R	13.36	9.660	3.702
line outage outside R	11.23	10.16	1.070

angles in degrees

area and an external angle due to power flows from other areas. The internal and external area angles offer more specific stress information about the area.

Based on (15), (24), and (22), the power flowing from M_a to M_b decomposes as

$$P_{ab} = P_a^{\text{into}} + P_a^{\text{inR}}.$$
(46)

 P_a^{into} is the power flowing into area R along the external tie lines attached to the border buses in M_a . $P_a^{\text{inR}} = \sigma_a P_m - \sigma_a B_{mn} B_{nn}^{-1} P_n$ is the total equivalent power injected into the buses M_a that corresponds to power injected in R. Moreover, since $P_{ab} = -P_{ba}$

$$P_{ab} = P_a^{\text{into}} + P_a^{\text{inR}} = -P_b^{\text{into}} - P_b^{\text{inR}}$$
$$= \frac{1}{2} \left(P_a^{\text{into}} - P_b^{\text{into}} \right) + \frac{1}{2} \left(P_a^{\text{inR}} - P_b^{\text{inR}} \right).$$
(47)

Equation (47) decomposes P_{ab} into two parts. The first part is related to the difference of the external power flows injected at buses M_a and M_b . The second part is related to the difference of the power flows equivalent to the power injected in area R. Dividing (47) by the susceptance b_{ab} and using the Ohm's law (8) decomposes the angle across the area as

$$\hat{\theta}_{ab}^{\text{into}} = \frac{F_a^{\text{into}} - F_b^{\text{into}}}{2b_{ab}} \quad \text{and} \quad \hat{\theta}_{ab}^{\text{inR}} = \frac{F_a^{\text{into}} - F_b^{\text{into}}}{2b_{ab}}.$$
 (49)

ł

The angle $\hat{\theta}_{ab}^{into}$ is caused by differences in the powers entering into the area at M_a and M_b and measures the external stress on area R. The angle $\hat{\theta}_{ab}^{inR}$ is caused by the differences at M_a and M_b of the powers equivalent to the powers generated or consumed inside area R and measures the internal stress on R. In particular, $\hat{\theta}_{ab}^{inR}$ only depends on the generation and loads inside R and the lines in service inside R.

The internal angle $\hat{\theta}_{ab}^{inR}$ can be obtained from additional tie line phasor measurements as follows. The tie line power flowing into a bus can be obtained from phasor measurements if both the currents in the tie lines and the bus voltage are measured. If the external tie line power entering each bus in M_a is known from these measurements, these can be summed to obtain the total power P_a^{into} entering into the area R through M_a . The total power P_b^{into} entering into R through M_b can be obtained similarly. Then we can obtain $\hat{\theta}_{ab}^{\text{into}}$ using (49) and then obtain $\hat{\theta}_{ab}^{\text{inR}}$ using $\hat{\theta}_{ab}^{\text{inR}} = \hat{\theta}_{ab} - \hat{\theta}_{ab}^{\text{into}}$.

Line outages can be modeled by equal and opposite power injections at either end of the line whose magnitude is proportional to the power flow on the line before it outaged [8]. Since the angles across the area respond proportionally to the magnitude of the equal and opposite power injections, the change in the area angles when the line outages is proportional to the power flow on the line before it outaged. Formulas quantifying this relationship using generalized line outage distribution factors are given in [4].

In the example of area R in Fig. 8, the base case area angle $\hat{\theta}_{ab} = 10.85$ degree decomposes into the external angle $\hat{\theta}_{ab}^{into} = 9.776$ degree and the internal angle $\hat{\theta}_{ab}^{inR} = 1.070$ degree. It can be seen from Table V that all the transfers and line outages shown in Fig. 8 affect the external angle $\hat{\theta}_{ab}^{into}$. However, the internal angle $\hat{\theta}_{ab}^{inR}$ only changes when the transfer or line outage is inside area R.

D. Discussion

We suggest using total, internal, and external area angles to monitor stress across a power system area. The angles can be easily calculated from phasor measurements at all the buses along the border of the area using a DC load flow model of the area and knowledge of which lines in the area are in service. It is natural to place phasor measurements at area tie lines. The stress monitoring shows one way to gain value from placing phasor measurements at all the tie lines to an area.

Previous approaches to measuring stress with phasor measurements have used the difference of angles at two buses or searched for patterns in angles from many buses. The area angles have some advantages over these approaches. The area angles give stress information specific to an area of the power system. This corresponds with the way large power systems are operated, and information that describes specific properties of a specific area of a large power system is more actionable. The area angles respond to line outages and power redispatches in accordance with circuit laws. The internal angle only changes when lines inside the area outage or when there is redispatch of power within the area, and can therefore be used to detect changes and confirm that they happened inside the area.

VIII. CONCLUSION

This paper defines and explains from first principles a new concept of voltages across an area of a network. The new volt-

ages are weighted combinations of voltages at the border buses of the area. The voltages can be real voltages in a resistive network, voltage angles in a DC load flow, or complex voltages in an AC load flow. Ohm's law is satisfied in each of these cases as detailed in Table I. The voltages across an area are obtained by a standard Kron reduction of the area to a network joining the border buses, followed by a contraction of the border buses into a network equivalent to the area that typically has two or three buses. The contraction of the border buses relies on a new concept of voltage across a cutset of lines.

Voltage angles across an area are a combination of individual bus voltages at the border of the area. The area conductance and currents through the area also combine together individual line conductances and individual currents to describe bulk electrical characteristics of the area. (Remember to substitute these quantities appropriately for DC and AC load flow as shown in Table I; for example, in DC load flow, the "currents" become real powers.) Since the area voltages, conductances, and currents obey circuit laws, they are more meaningful and useful than an arbitrary combination of circuit quantities. In particular, they will behave in ways consistent with the intuition of power system operators and engineers.

The applications of the new area voltages include model reduction, monitoring area stress, and detecting line trips from synchrophasor measurements [2]–[4]. The explanations of area voltages in this paper illustrate systematic AC and DC load flow reductions for single and multiple areas. We also describe how area angles measure the area stress and can be obtained from synchrophasor measurements of voltages at the border buses of an area. If the currents or powers flowing into the area at the border buses are also measured, then an internal angle that only responds to changes inside the area can also be monitored. We refer to [4] for initial work applying area angles to detecting line trips in areas.

All of these applications depend on a systematic circuit theory foundation and formulas to compute the area voltages, conductances, and currents. This paper provides this foundation and derives those formulas.

APPENDIX A

CUTSET VOLTAGE DERIVED FROM NETWORK THEORY

We derive the cutset voltage \hat{V}_{ab} as an instance of classical circuit theory using a novel and nonstandard choice of basis. Chen in [1] explains a generalized cutset analysis of a resistive network with voltage and current sources. We now state Chen's equations. Let the rows of the matrix Q specify a basis for the cutset vector space of the network. (The cutset vector space of the network is spanned by the rows of A and has dimension nbus-1 for a connected network [1], [11].) Write \hat{V}_q for the (generalized) cutset voltages, I_q for the (generalized) cutset currents, G_q for the (generalized) cutset admittance matrix, and I_b for the branch current flows. Then

$$\hat{V} = Q^T \hat{V}_a \tag{50}$$

$$I_a = QI_b \tag{51}$$

$$G_a = Q \Lambda Q^T \tag{52}$$

and
$$I_q = G_q \hat{V}_q$$
 (53)

which is Chen's equation (2.81). Another good explanation of the cutset vector space and (50)–(53) is by Bryant in [11, section 8], where (53) appears as Bryant's equation (56).

Now we make a special choice of the basis for the cutset vector space of the network by choosing the first row of the Q matrix to be the cutset c so that

$$Q = \begin{pmatrix} c \\ Q_{\perp} \end{pmatrix}$$
(54)

and so that the remaining rows Q_{\perp} of Q are basis vectors of the cutset vector space orthogonal to c in the sense that

$$c\Lambda Q_{\perp}^T = 0. \tag{55}$$

(It is often impossible to choose all rows of Q_{\perp} to consist of vectors with entries ± 1 and zero corresponding to the usual cutsets, but this causes no fundamental difficulty.) Then in the basis (54), multiplying (50) on the left with $c\Lambda$ gives

$$c\Lambda \hat{V} = c\Lambda Q^T \hat{V}_q = c\Lambda c^T \hat{V}_{q1} = g_{ab} \hat{V}_{q1}$$
(56)

where \hat{V}_{q1} , the first component of \hat{V}_q , is the generalized cutset coordinate associated with *c*. Comparison of (56) with (7) shows that $\hat{V}_{ab} = \hat{V}_{q1}$ is the generalized cutset coordinate associated with *c* in the basis (54). Moreover, in the basis (54), we have from (52) that

$$G_q = Q\Lambda Q^T = \begin{pmatrix} c\Lambda c^T & 0\\ 0 & Q_{\perp}\Lambda Q_{\perp}^T \end{pmatrix} = \begin{pmatrix} g_{ab} & 0\\ 0 & Q_{\perp}\Lambda Q_{\perp}^T \end{pmatrix}$$

and the first components of (51) and (53) become, respectively,

$$I_{q1} = cI_b = \sigma_a AI_b = \sigma_a I_m^{\text{red}} = I_{ab} \quad \text{and} \quad I_{q1} = g_{ab} \hat{V}_{q1}.$$

Hence, the first component of (53) may be written as $I_{ab} = g_{ab}\hat{V}_{ab}$, which is (17). Thus, we have found a nonstandard cutset basis (54) in which the cutset voltage \hat{V}_{ab} is the generalized cutset coordinate associated with the basis element c.

As an example, consider the network of Fig. 1 with all the line conductances equal to 1. Then Λ is an identity matrix and a Q matrix satisfying (55) is

$$Q = \begin{pmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 0 & 0 & 0 \\ 1 & 3 & -2 & 0 & -2 & 0 & 3 & 0 & 2 \\ -2 & 0 & 1 & 3 & -2 & 0 & -3 & 3 & 2 \\ -2 & 0 & -2 & 0 & 1 & 3 & 0 & -3 & 2 \end{pmatrix}.$$

APPENDIX B Shunts to a Ground Node

Sometimes the area R has all buses joined by shunts to a ground node g that is only implicitly included in R. This appendix reviews the adjustments needed.

Ohm's law for area R together with the ground node is

$$\begin{pmatrix} G_{rr}^{\rm R} & G_{rg} \\ G_{gr} & G_{gg} \end{pmatrix} \begin{pmatrix} V_r \\ V_g \end{pmatrix} = \begin{pmatrix} I_r^{\rm R} \\ I_g \end{pmatrix}.$$
 (57)

The rows of (57) sum to zero so that $\mathbf{1}G_{rr}^{\mathrm{R}} = -G_{gr}$ and $\mathbf{1}I_{r}^{\mathrm{R}} = -I_{g}$. We can choose $V_{g} = 0$ in (57) and instead use $G_{rr}^{\mathrm{R}}V_{r} = I_{r}^{\mathrm{R}}$. Note that $\mathbf{1}G_{rr}^{\mathrm{R}} \neq 0$ and G_{rr}^{R} is invertible. If the conductance matrix of area R neglecting shunts and the ground node is $G_{rr}^{\mathrm{Rnoshunt}}$, then $G_{rr}^{\mathrm{R}} = G_{rr}^{\mathrm{Rnoshunt}} - \operatorname{diag}\{G_{gr}\}$.

APPENDIX C

EQUIVALENCE OF MULTIAREA AND SINGLE AREA

We show that the same voltages V_A across area R_{γ} are obtained by simultaneous reduction of the three areas in Section VI and by the single area computation of Section IV.

We order the interior buses in the order of the areas. Then in Section VI, the network conductance matrix becomes

$$G = \begin{pmatrix} G_{mm} & G_{m\gamma} & G_{m\alpha} & G_{m\beta} \\ G_{\gamma m} & G_{\gamma \gamma} & 0 & 0 \\ G_{\alpha m} & 0 & G_{\alpha \alpha} & 0 \\ G_{\beta m} & 0 & 0 & G_{\beta \beta} \end{pmatrix} \text{ and }$$
$$G_{mm}^{\text{red}} = G_{mm} - G_{m\gamma}G_{\gamma\gamma}^{-1}G_{\gamma m} - G_{m\alpha}G_{\alpha \alpha}^{-1}G_{\alpha m} - G_{m\beta}G_{\beta\beta}^{-1}G_{\beta m}.$$

In Section IV, using (21)

$$\begin{aligned} G_{mm}^{\mathbf{R}_{\gamma}} &= G_{mm} + \operatorname{diag}\{G_{m\alpha}\mathbf{1}\} + \operatorname{diag}\{G_{m\beta}\mathbf{1}\},\\ G^{\mathbf{R}_{\gamma}} &= \begin{pmatrix} G_{mm}^{\mathbf{R}_{\gamma}} & G_{m\gamma} \\ G_{\gamma m} & G_{\gamma\gamma} \end{pmatrix}\\ \text{and } G_{mm}^{\mathrm{Rred}} &= G_{mm}^{\mathbf{R}_{\gamma}} - G_{m\gamma}G_{\gamma\gamma}^{-1}G_{\gamma m}. \end{aligned}$$

Hence

$$G_{mm}^{\text{red}} = G_{mm}^{\text{Rred}} - \text{diag}\{G_{m\alpha}\mathbf{1}\} - G_{m\alpha}G_{\alpha\alpha}^{-1}G_{\alpha m} - \text{diag}\{G_{m\beta}\mathbf{1}\} - G_{m\beta}G_{\beta\beta}^{-1}G_{\beta m}.$$

Since

$$-\operatorname{diag}\{G_{m\alpha}\mathbf{1}\}V_m - G_{m\alpha}G_{\alpha\alpha}^{-1}G_{\alpha m}V_m$$

=
$$-\operatorname{diag}\{G_{m\alpha}\mathbf{1}\}V_m + G_{m\alpha}V_\alpha = I_m^{\alpha \text{into}}$$

is the currents $I_m^{\alpha \text{into}}$ entering border buses M from N_{α} ,

$$\sigma_A G_{mm}^{\text{red}} V_m = \sigma_A G_{mm}^{\text{Rred}} V_m + \sigma_A I_m^{\alpha \text{into}} + \sigma_A I_m^{\beta \text{into}}$$
$$= \sigma_A G_{mm}^{\text{Rred}} V_m + \sigma_A I_m^{\text{into}}$$
$$= \sigma_A G_{mm}^{\text{Rred}} V_m + I_A^{\text{into}}.$$

Similarly

$$\begin{aligned} G_A V_A &= \sigma_A G_{mm}^{\text{red}} \sigma_A^T V_A \\ &= \sigma_A G_{mm}^{\text{Rred}} \sigma_A^T V_A - \text{diag} \{ G_{A\alpha} \mathbf{1} \} V_A \\ &- G_{A\alpha} G_{\alpha\alpha}^{-1} G_{\alpha A} V_A \\ &- \text{diag} \{ G_{A\beta} \mathbf{1} \} V_A + G_{A\beta} V_A \\ &= \sigma_A G_{mm}^{\text{Rred}} \sigma_A^T V_A + I_A^{\text{cinto}} + I_A^{\beta \text{into}} \\ &= \sigma_A G_{mm}^{\text{Rred}} \sigma_A^T V_A + I_A^{\text{cinto}}. \end{aligned}$$

Hence, $G_A V_A = \sigma_A G_{mm}^{\text{red}} V_m$ is equivalent to $\sigma_A G_{mm}^{\text{Rred}} \sigma_A^T V_A = \sigma_A G_{mm}^{\text{Rred}} V_m$, and both equations give the same voltages V_A .

APPENDIX D

PARAMETERS OF 39-BUS NEW ENGLAND TEST SYSTEM

For the DC power flow, the bus power injections and bus angles are P = 0, 0, -3.22, -5., 0, 0, -2.338, -5.22, 0, 0, 0, -0.085, 0, 0, -3.2, -3.294, 0, -1.58, 0, -6.8, -2.74, 0, -1.58, 0, -5.58, 0

0, -2.475, -3.086, -2.24, -1.39, -2.81, -2.06, -2.835,2.5, 5.6373, 6.5, 6.32, 5.08, 6.5, 5.6, 5.4, 7.81841, -0.98271 per unit and $\theta = 1.622, 4.290, 1.125, 0.2740, 1.499, 2.218,$ -0.02249, -0.5267, -0.2148, 4.688, 3.835, 3.782, 3.940,2.219, 1.794, 3.334, 2.188, 1.323, 8.473, 7.113, 5.923, 10.81, 10.58, 3.486, 5.418, 3.809, 1.785, 7.103, 9.934, 6.882, 10.29, 12.14, 13.61, 12.35, 16.13, 19.31, 12.60, 16.92, 0 degrees. The start and end buses of the lines are 1-2, 1-39, 2-3, 2-25, 3-4, 3-18, 4-5, 4-14, 5-6, 5-8, 6-7, 6-11, 7-8, 8-9, 9-39, 10-11, 10-13, 13-14, 14-15, 15-16, 16-17, 16-19, 16-21, 16-24, 17-18, 17-27, 21-22, 22-23, 23-24, 25-26, 26-27, 26-28, 26-29, 28-29, 2-30, 31-6, 10-32, 12-11, 12-13, 19-20, 19-33, 20-34, 22-35, 23-36, 25-37, 29-38 and the line reactances are X = 0.0411, 0.025, 0.0151, 0.0086, 0.0213,0.0133, 0.0128, 0.0129, 0.0026, 0.0112, 0.0092, 0.0082, 0.0046, 0.0363, 0.025, 0.0043, 0.0043, 0.0101, 0.0217, 0.0094, 0.0089, 0.0195, 0.0135, 0.0059, 0.0082, 0.0173, 0.014, 0.0096, 0.035, 0.0323, 0.0147, 0.0474, 0.0625, 0.0151, 0.0181, 0.025, 0.02, 0.0435, 0.0435, 0.0138, 0.0142, 0.018, 0.0143, 0.0272, 0.0232, 0.0156 per unit. For the AC power flow, see [12] and set the transformer tap ratios to unity.

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